

FIG. 1

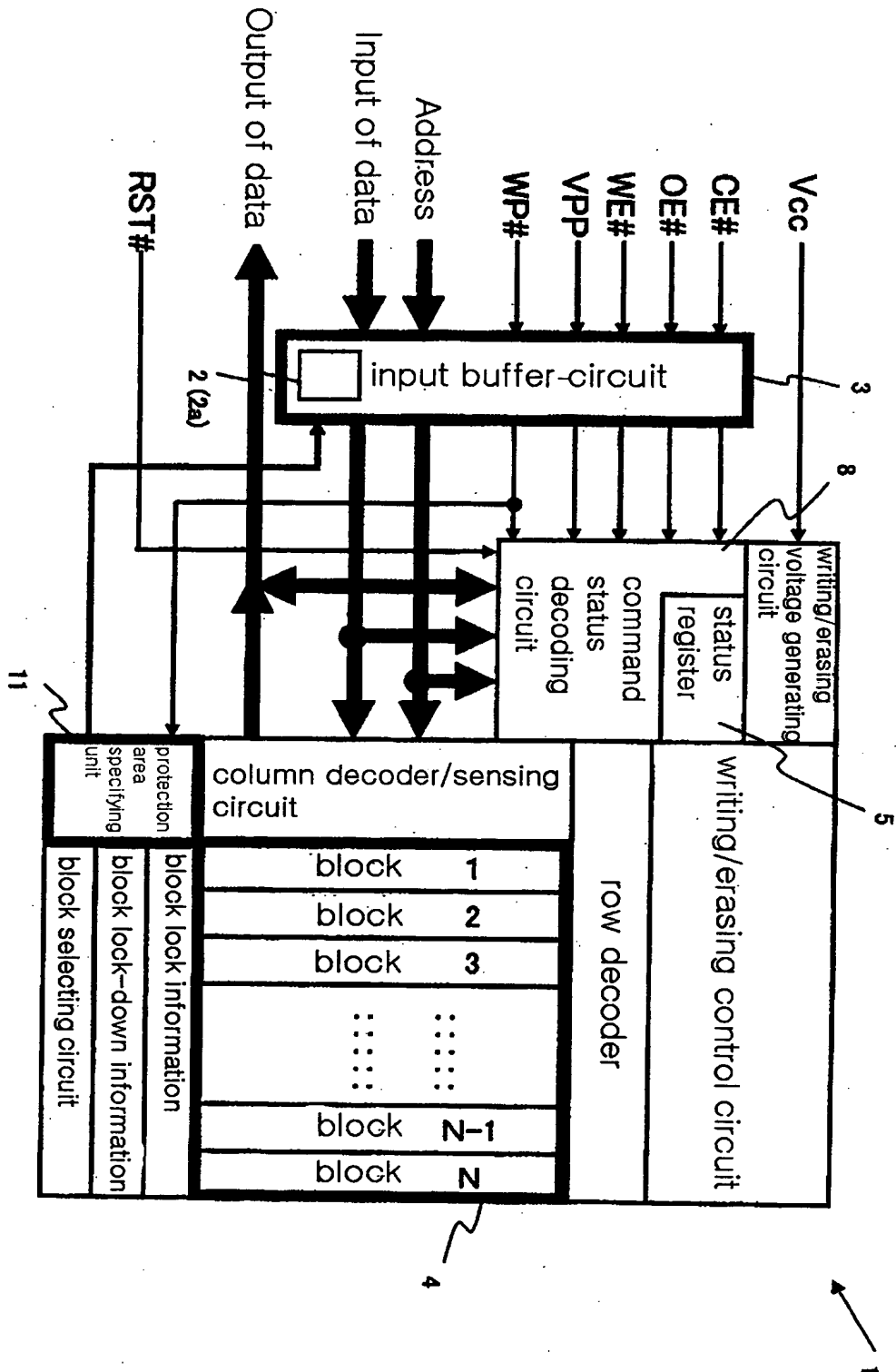


FIG. 2

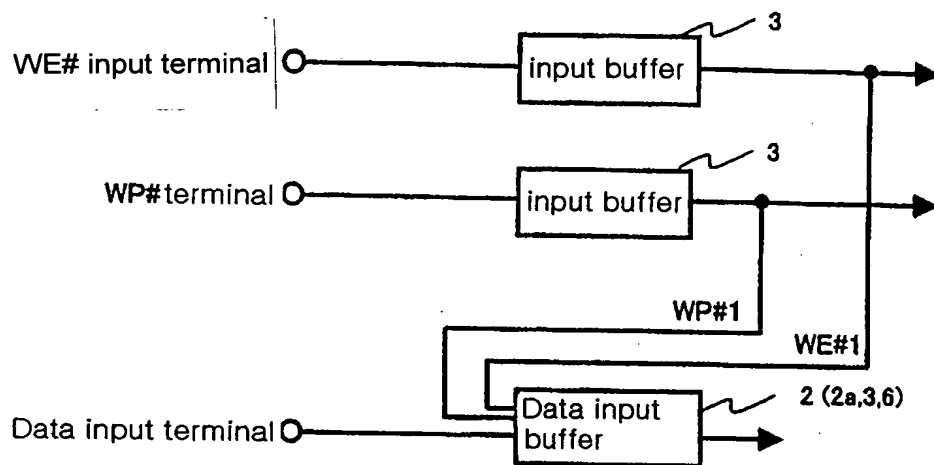


FIG. 3

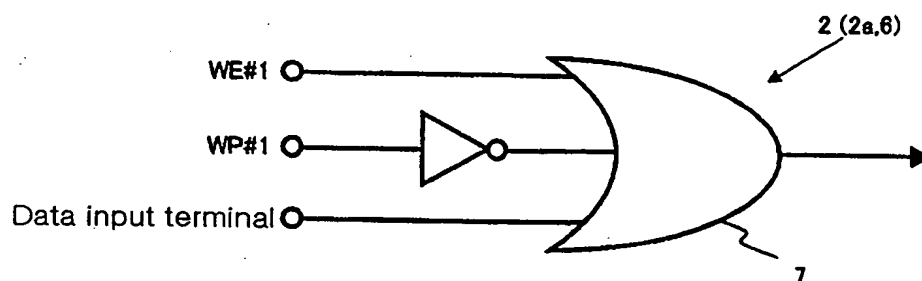


FIG. 4

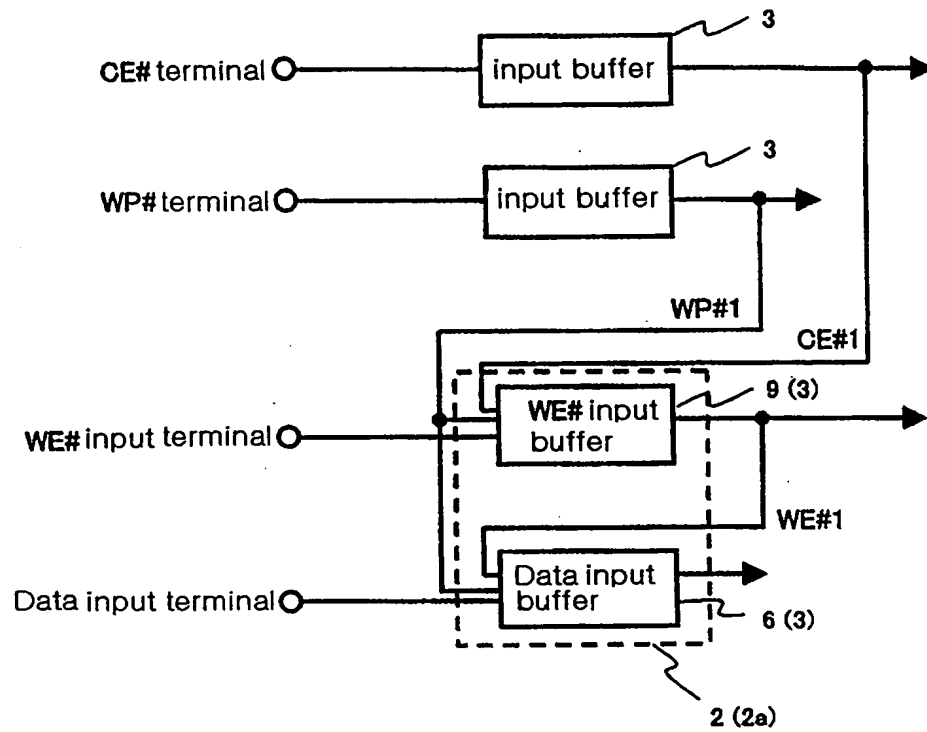


FIG. 5

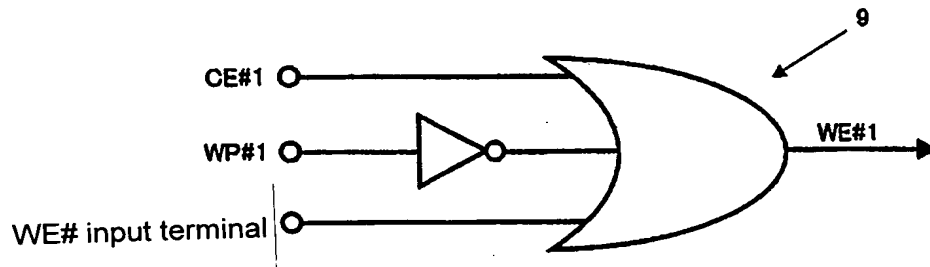


FIG. 6

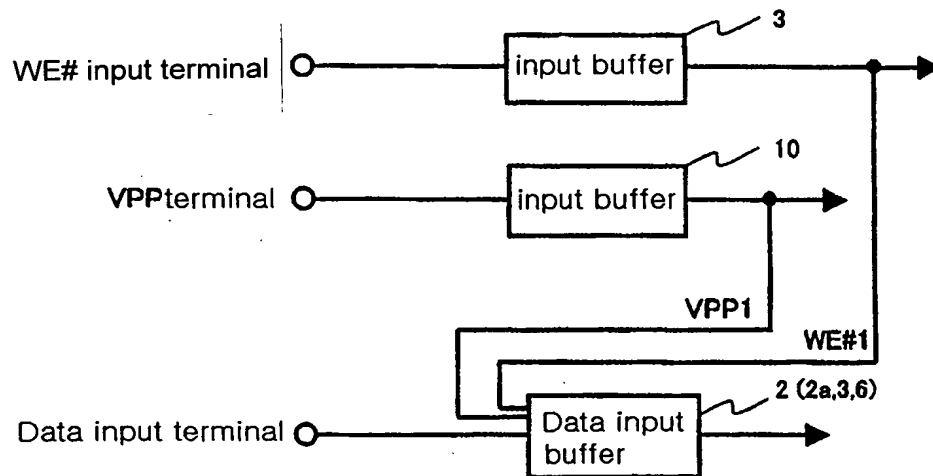


FIG. 7

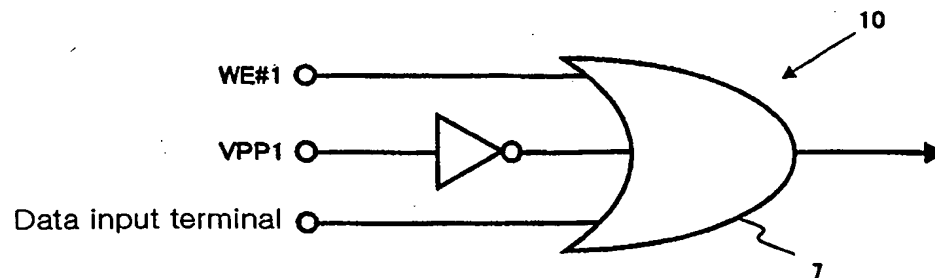


FIG. 8

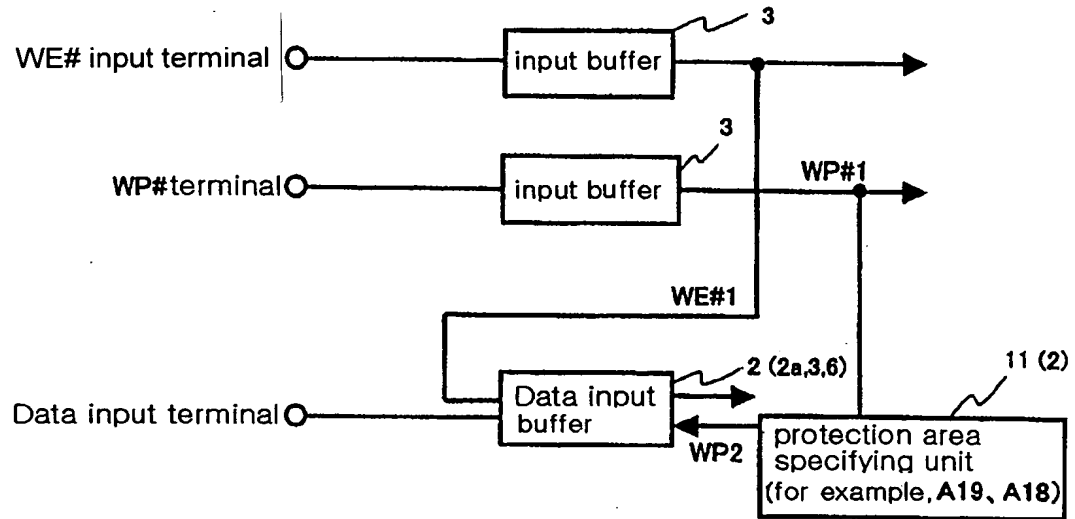


FIG. 9

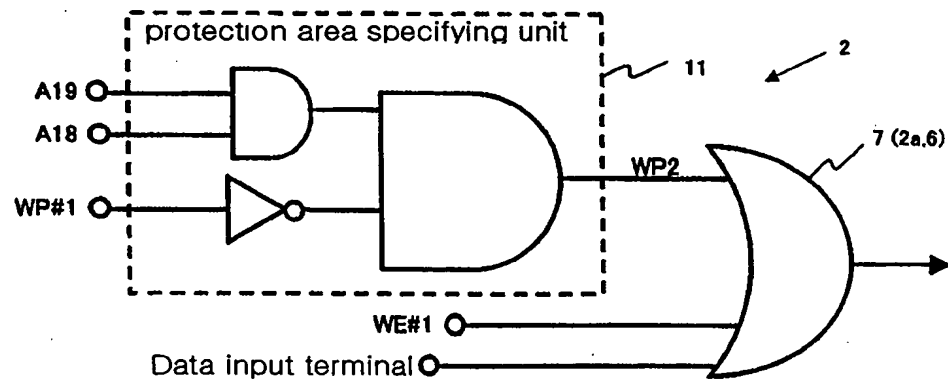
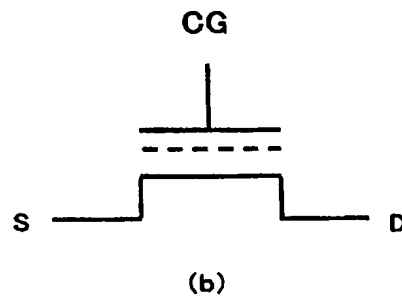
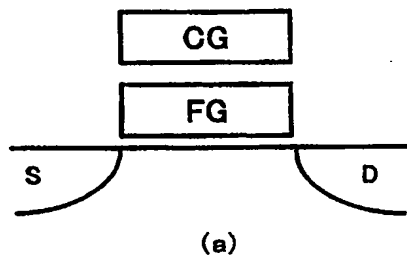


FIG. 10

Prior Art



Inventor: Katsumi FUKUMOTO

Title: ERRONEOUS OPERATION PREVENTING CIRCUIT OF
NON-VOLATILE MEMORY DEVICE

REPLACEMENT SHEET

FIG. 11

Prior Art

